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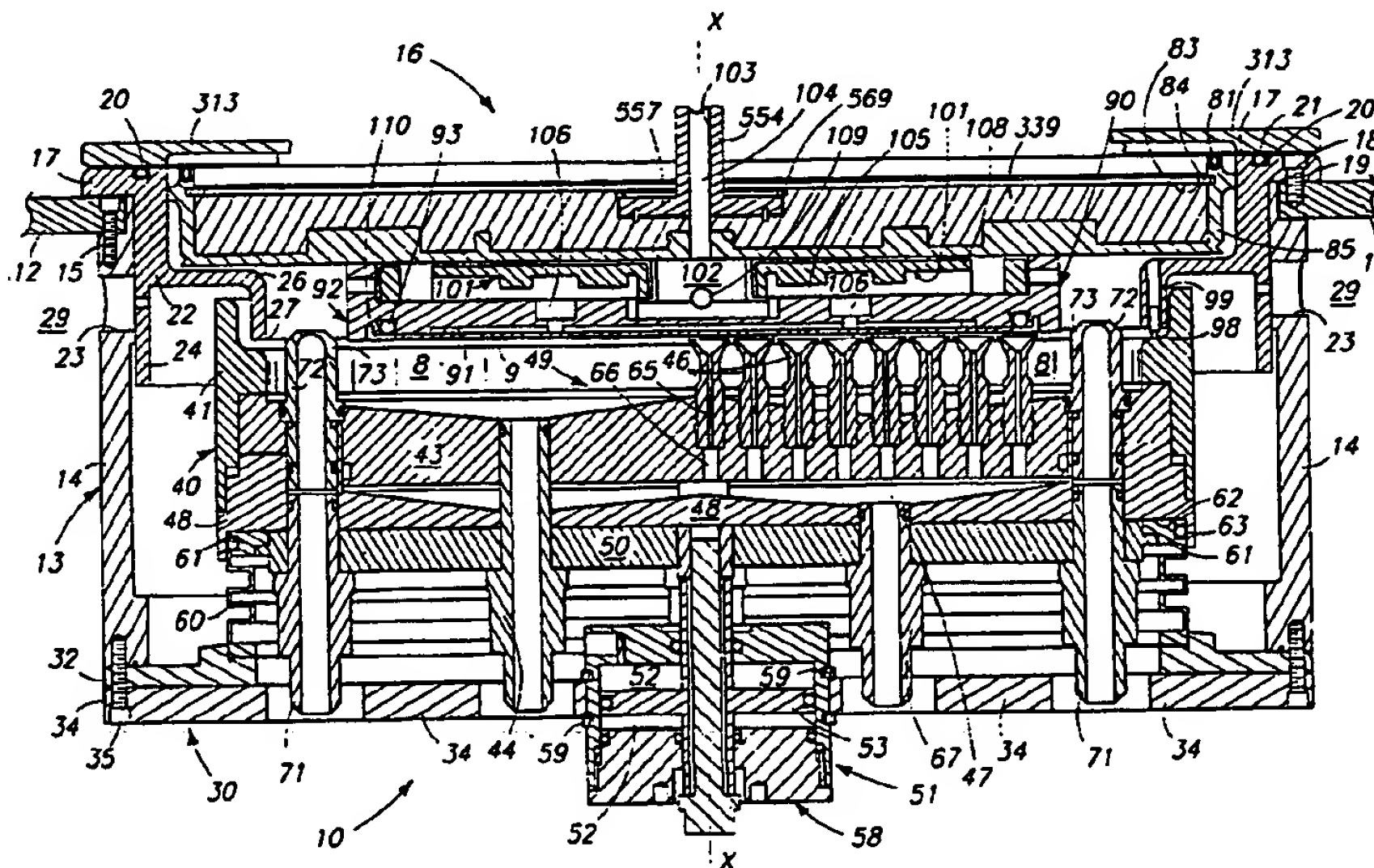
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(54) Title: SEMICONDUCTOR PROCESSING WITH NON-JETTING FLUID STREAM DISCHARGE ARRAY



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DESCRIPTION**SEMICONDUCTOR PROCESSING WITH NON-JETTING
FLUID STREAM DISCHARGE ARRAY****Technical Field**

5 This invention relates to apparatus and methods for processing semiconductors using liquids which produce gaseous products.

Background Art

The production of semiconductor devices, such as wafers, substrates, resulting integrated circuits, flat panel displays, data discs and other devices, often involves a variety
10 of chemical processing steps. In some processes a liquid reactant is used which produces gaseous products. The production of gaseous products at the interface between the liquid processing fluid and the semiconductor piece can significantly effect the action of the reactant liquid upon the treated surface of the semiconductor material.

One example of chemistry which produces gaseous products involves the etching of
15 silicon wafers (Si) using nitric acid (HNO_3) and hydrogen fluoride (HF). The reactions between these acids and silicon produce nitrous oxides of various stoichiometric ratios, most typically considered nitrogen dioxide (NO_2). The production of gases at the surface of the semiconductor piece causes the rate of reaction to be governed by diffusion through the gas-containing boundary layer. This diffusion limited rate characteristic of these and other
20 reactions causes the processes to be more susceptible to rate variation across the wafer or other semiconductor piece. It also causes the reproducibility of the processes to vary to a greater degree than more typical liquid processes which are limited by chemical reaction rate mechanisms associated with the reactants rather than diffusion characteristics as in the reactions noted above.

25 Spray liquid and liquid immersion processes have previously been used to perform the gas-producing reactions explained above. These processes have suffered from the disadvantage that they expose both sides of a wafer to the chemical reaction and have variations in rate and reproducibility. Approaches to limit or protect exposure of the back side of the wafer have not been satisfactory because they do not fully protect the wafer or
30 require significant additional processing time or additional processing materials.

Accordingly, there has remained a strong need for improved processing apparatus and improved processes for performing gas producing semiconductor processing.

Brief Description of the Drawings

One or more preferred forms of the invention are described herein with reference
35 to the accompanying drawings. The drawings are briefly described below.

Fig. 1 is a longitudinal sectional view showing the processing bowl and portions of a processing head forming parts of a preferred embodiment of the invention.

Fig. 2 is a top view of the bowl portion shown in Fig. 1.

Fig. 3 is an enlarged view showing several liquid emitters in the process of applying
5 processing liquid to a portion of the treated surface of a semiconductor wafer.

Fig. 4 is a longitudinal sectional view showing additional portions of the processing head shown in Fig. 1.

Fig. 5 is a cross-sectional view taken along line 5-5 of Fig. 4.

Fig. 6 is a cross-sectional view taken along line 6-6 of Fig. 4.

10 **Best Modes for Carrying Out the Invention and Disclosure of Invention**

The Figs. show pertinent portions of a preferred semiconductor processor 10 according to this invention. Processor 10 includes a deck 12 which forms a part of the frame. Deck 12 mounts a bowl assembly 13. A processing head 16 is movably mounted relative to the frame and bowl assembly using a suitable robotic arm (not shown). Processing head 16
15 is movable between loading positions wherein the head is removed from the bowl assembly and a processing position shown in Fig. 1. In the loading positions the processing head can be loaded with a semiconductor piece, such as semiconductor wafer 9. In the processing position, head 16 is in mating relationship with the bowl assembly to provide a substantially enclosed processing chamber 8. Processing chamber 8 forms part of a larger contained
20 volume defined between bowl chassis 14, inner bowl subassembly 40, and the processing head 16, when head 16 is in the processing position.

Bowl assembly 13 includes a bowl chassis 14. Bowl chassis 14 is mounted to the deck using fasteners 15 which extend through apertures formed through the deck and into threaded receptacles formed in upper portions of the bowl chassis. Bowl chassis 14 extends
25 downward in a substantially cylindrical configuration to provide portions of a process containment vessel provided by the bowl assembly and processing head. Bowl chassis 14 includes lateral exhaust ports 23 which extend through the wall of the bowl chassis to an exhaust plenum 29 and allow removal of vapors and gases using a suitable exhaust line (not shown) and associated vacuum supply (not shown).

30 Bowl assembly 13 also includes a top bowl piece 17 which extends about the upper portions of the bowl and is fastened to the deck using mounting fasteners 18 which extend through an upper mounting flange 19. Flange 19 is advantageously provided with a seal 20 mounted in an associated seal groove 21. Seal 20 aides in providing a substantially sealed relationship between the bowl assembly 13 and processing head 16.

35 The top bowl piece also has a downward primary extension 22 which forms a cylindrical barrel. A secondary extension 24 extend downwardly from the primary extension

3

to form a mounting piece and diffusion baffle to communicate the evacuating action from the exhaust ports 23 about the periphery of the bowl assembly.

Top bowl piece 17 also includes a splash diverter 26 which extends inwardly and downwardly from the primary extension 22. The downward extending lip 27 of the splash
5 diverter forms an annular drip lip. The drip lip is in spaced relative position to upper portions of an inner bowl 41. This serves to help contain spray or splash of processing chemicals or rinse liquids flying within the processing chamber 8.

The bowl assembly further includes a bottom subassembly 30. The bowl bottom subassembly 30 preferably has a first or upper ring member 32 and a second or lower ring
10 member 34. Both ring members 32 and 34 are mounted to the lower edge of the bowl chassis 14 using fasteners 35.

The bowl assembly also includes an inner bowl subassembly 40. Subassembly 40 includes inner bowl piece 41. Inner bowl piece 41 is connected to an upper or first partition 43. First partition 43 forms a processing chamber floor which collects draining
15 processing liquid and channels it to a drain 44. The processing liquid is emitted from a plurality of upstanding liquid emitters 46. Processing liquid is supplied to the emitters 46 from an inter-partition supply chamber 47 which is sealed between first partition 43 and a second or lower partition 48. The upper and lower partitions are both connected to the inner bowl 41 by threading the inner bowl piece down over male threads formed on the
20 outside of lower partition 48.

The first partition 43 and associated liquid emitters 46 form a liquid distributor 49 which applies processing liquids to the treated lower side of wafer 9. The Figs. do not show all intended emitters 46, but only an exemplary sample. It is desired to have a full emitter array with emitters at various radial positions about the central axis X-X. Each liquid emitter
25 46 is preferably formed as a free-standing pillar or column extending toward the treated wafer surface from a suitable mounting location, such as from the processing chamber partition 43. As shown, this is preferably accomplished using individual emitter pieces which are received within emitter receptacles formed in the first partition 43. The pillar-shaped emitters 46 are preferably upstanding and provided with emission ports 61 (Fig. 3) from
30 which liquid processing chemicals are emitted in non-jetting streams.

Fig. 3 shows that the emission ports are preferably defined by spout portions 62 of the emitters. The spouts are preferably flared outwardly toward the emission ports which are circumscribed by spout rims 63. The spouts have an internal cup-shaped chambers 64 adjacent to the emission ports. The flared configuration of the spouts and associated cup
35 chambers act to slow the velocity of the flowing liquid chemicals and provide a larger emission port.

Emitter cup chambers 64 receive flows of liquid chemicals from emitter flow passageways 65 formed within the emitter pieces. The emitter flow passageways are in fluid communication with first partition supply passages 66 formed in the first partition 43. Passages 66 are in fluid communication with the inter-partition liquid supply chamber 47. Chamber 47 is supplied liquid via a liquid supply fitting 67 which extends through second partition 48.

Processing liquid is provided at a relatively slow flow rate so that a meniscus 76 (Fig. 3) is formed by surface tension extending from spout rims 63 upwardly toward the middle of the emission port in a liquid emission dome which is at least partially convex. The height of the meniscus dome is sufficient to be greater than the clearance gap 77 between the treated surface of the wafer and the emitter spout rims. This causes an emitter wash or washed area 78 to be wetted upon the treated surface for each emitter 46 when the clearance gap is within a suitable range. Preferred clearance gaps are in the range 0.010-0.100 inches (~0.3-2.5 millimeters), more preferably 0.020-0.070 inches (~0.5-1.8 millimeters). The spacing of the emitters 46 is preferably close but of sufficient spacing so the adjacent emitter washes on the treated surfaces are spaced to provide inter-wash gas escape channels 79.

The inner bowl assembly 40 is preferably constructed and mounted to allow extension and retraction relative to the wafer 9. As shown, this is accomplished using an inner bowl support plate 50 which connects with the lower partition 48. An extension actuator or operator 51 is controlled to move the inner bowl assembly into adjustable extended and retracted positions. As shown the actuator 51 moves the inner bowl assembly vertically upward or downward within a structurally limited range and as desired and controlled.

The extension operator 51 is supported upon the lower bottom wall piece 34 using opposing retainer rings 59 (not shown). The operator includes an operator housing 58 which encloses an internal chamber 52. Inside of the internal chamber is a slidably movable piston assembly 53. Pneumatic pressure is applied to the appropriate side of the piston to force the piston assembly upwardly or downwardly relative to the housing. This forces the support plate 50 and lower partition 48 upwardly and downwardly to position the inner bowl assembly 40 and attached liquid distributor 49. This enables the clearance gap 77 to be adjustable. It also allows the wafer to be positioned into the processing chamber when the processing head is moved into mating relationship with the bowl assembly, and thereafter the clearance gap can be reduced in preparation for the actual treatment process.

The inner bowl assembly movement is preferably limited by an adjustable stop system. As shown the preferred stop system uses three adjustable threaded stop pieces 98 which are

threaded into bores. The bores are advantageously formed at enlarged bosses 99 in top bowl piece 17.

The inner bowl assembly is preferably also provided with a flexible bellows member 60. Bellows 60 provides flexible sealing against vapors and liquids which enter the outer portions of the containment chamber, outside of process chamber 8. The bellows allow specific axial positioning of the inner bowl assembly and liquid distributor as just explained by the extension operator 51. The top portion of bellows 60 includes a top bellows flange 61. The top bellows flange is positioned within a mounting receptacle 62 formed in the lower portion of the second partition 48. A peripheral seal 63 is held within a groove formed about the outer surface of the top bellows flange. A bellows top piece is formed by support plate 50 which extends across the top of the bellows and is fastened to the second partition 48 using suitable fasteners (not shown).

Liquid supply fitting 67 is mounted in the bellows top piece through a mounting aperture formed therein. A liquid drain or outflow fitting 44 is also mounted in the bellows top piece and extends through both partitions 43 and 48 to remove liquid which has drained from the liquid emitters 46. The bellows top piece also supports fittings 71 which are in fluid communication with spray pipes 72. Spray pipes 72 are positioned laterally from the wafer and include jet openings 73 which direct a spray of rinse water or other rinse liquid across the lower treated surface of wafer 9. There are preferably two spray pipes 72.

The construction of the preferred processing head 16 will now be described in greater detail. Fig. 1 shows portions of processing head 16. Additional portions and details of the processing head are shown in Figs. 4-6. As shown, processing head 16 includes a shroud 313 which is the main structural part of the head. Shroud 313 is mounted to a robotic arm (not shown) which is used to move the head into the mating position shown in Fig. 1, and removed loading positions (not shown) wherein wafer 9 can be loaded onto the processing head using a wafer handling device of conventional construction (not shown).

A rotating assembly forming a part of the processing head is mounted for rotation relative to the shroud. The rotating assembly includes a rotor having an inner rotor piece 339. The inner rotor piece is constructed to fit within a receptacle 81 formed within the upper face of an outer rotor piece 85. The outer rotor piece is provided with a groove 84 which mounts a snap ring or other suitable removable retainer ring 83. Retainer ring 83 secures the inner and outer rotor pieces together.

The rotor is provided with a suitable wafer support or other means for holding the wafer 9 or other semiconductor piece in position. The wafer is held upon the rotor against the force of gravity. It is also held for rotation by the rotor during processing. As shown, processing head 16 is provided with a vacuum chuck wafer holder 90 which supports the

wafer. Vacuum chuck 90 has a vacuum recess 91 formed in the lower face of the wafer support piece 92. The wafer support piece 92 is secured to the outer rotor piece 85 using fasteners (not shown). The wafer support face of piece 92 is also advantageously provided with an edge seal 93 which is held with an associated edge seal groove. The outer peripheral
5 edge of the wafer contacts the wafer support piece 92 at the edge seal 93. This sealing contact along the wafer periphery serves to enhance vacuum retention of the wafer and further helps prevent processing liquids and gases existing within the processing chamber from migrating across the back or upper surface of wafer 9. This reduces or eliminates the need for any back surface protection for the wafer, such as coatings or temporary overlays.

10 The vacuum chuck wafer holder also preferably includes a pair of aspirators 101. Both aspirators are rectangular bars extending radially outward from the centerline or rotational axis of the rotating assembly. Aspirators 101 can be selected from commercially available products of varying specific construction. Aspirators 101 are supplied with a suitable pressurized gas, such as compressed nitrogen or clean dry air via an aspirator supply
15 plenum 102. Plenum 102 is supplied with gas via a rotor shaft internal passageway 103 and rotor piece gas supply passageways 104.

As shown aspirators 101 have a body 108 which are provide with gas passageways 105. The gas passageways are substantially enclosed except near the ends. Gas passes from the plenum 102 and produces a vacuum pressure in vacuum counterbores 106
20 according to the well-known Venturi effect. The vacuum pressure communicated via bores 106 is applied to the vacuum recess 91 and back surface of wafer 9 to retain the wafer in position upon the wafer holder.

Pressurized gas within plenum 102 is also communicated via an edge supply gallery 109 to an annular edge blanket gas supply groove 110, via additional passageways (not
25 shown in the sectional view of Fig. 1). This construction allows the wafer support head to provide both a vacuum for retaining the wafer and pressurized gas for blanketing or purging the edge of the wafer with a protective gas purge, using a single pressurized gas supply via passageway 103 and associated gas supply fitting 112 (Fig. 4).

Fig. 4 shows that the processor head assembly 16 also includes a cap 560 which
30 covers a drive assembly that is described more fully below. The wafer support rotor drive assembly includes a motor 559 which is mounted upon motor support 558. Motor 559 is preferably a brushless DC motor. Motor 559 has a hollow outer motor shaft 553 supported by two sets of ball bearings 555. The hollow motor shaft 553 has a flanged shaft head 556 at its lower end. The hollow motor shaft 553 receives an inner motor shaft 554 therein. The
35 inner motor shaft has a disk-shaped head 557 which is suitably connected to the wafer support plate 539, such as by being received and connected within the wafer support plate

motor mounting receptacle 568. The disk-shaped head 557 is firmly held in this receptacle using a ring retainer 569. The upper end of the inner shaft 554 is held in position by retaining nut 589.

The angular positions of the rotating assembly about rotational axis X-X are preferably controlled to assume a particular desired position when the rotatable wafer support 530 stops. This indexing of the stationary position of the rotor is desired for assurance of wafer positioning when loading and unloading the processing head to provide proper and unique positional engagement of the wafer by a wafer handling paddle or other wafer handler (not shown).

10 Figs. 5 and 6 show an indexing means 750 used to position the wafer support, motor and other rotatable parts forming the rotating assembly of the processing head drive. Rotor positioning or indexing mechanism 750 includes a cammed rotor plate 759 mounted to rotate with motor shaft 553. The cam plate 759 has a single low point or perigee which is precisely defined by detent 760. Detent 760 allows the rotor and wafer support member to be
15 positioned in a single desired rotational position when stopped. The outer periphery of the cam plate is sloped radially from a high point or apogee which is opposite detent 760 toward the perigee at the detent.

A roller 761 is mounted at one end of a crank arm 762. Crank arm 762 is pivotally supported on a supporting shaft 763. The opposite end of crank arm 762 includes a pivotal
20 connection 764 at which is joined the outer end of a pneumatic ram assembly 766.

The pneumatic ram 766 is normally spring biased into a retracted position wherein roller 761 is radially clear of the cammed rotor plate 759. To index the rotating assembly the roller is extended inwardly to engage the cammed rotor plate. Roller 761 is extended by pneumatic ram 766 which pivots arm 762 upon which the roller is mounted. The
25 roller 761 is extended inward when the rotating assembly and cam plate 759 are nearly stopped. The roller bears against the edge of rotor plate 759 to stop the rotor when roller 761 is received in detent 760. The detent assures that the fingers 534 are uniquely indexed to thereby facilitate interaction with related wafer transfer equipment (not shown).

A rotor position and motion monitoring subsystem is also advantageously provided
30 within processing head 512 for measuring the speed and direction of rotation of the wafer plate 530 about the rotational axis X-X. This subsystem also allows confirmation of the position of the rotor after ram 766 is extended. The rotor position and motion monitoring subsystem includes a rotor indicating element such as rotor indicator disk 754, best shown in Fig. 6. Indicator disk 754 is provided with a series of peripheral notches 755 which
35 intermittently pass and interrupt three optical beams. The three optical beams are provided by paired emitters and detectors (not shown) which beam through the notches on disk 754.

Two optical emitter detector pairs are mounted upon bracket 785. These two are aligned to intermittently pass two beams through notches 755. The two emitter detector pairs are spaced so that they are out of phase from one another by an amount which produces a different phase shift depending upon whether the direction is clockwise or counter-clockwise.

- 5 The third emitter detector pair is mounted upon mounting bracket 786 and is aligned to intermittently pass through the deeper notches and confirm proper positioning after ram 766 has been extended.

The invention is used in a variety of processing applications in the manners described hereinabove. The various components are made using a variety of metal and plastic forming
10 techniques, such as machining or molding into the parts shown and described herein.

The invention further includes novel processes or methods for treating semiconductor pieces. Exemplary methods include the etching of silicon or the chemical removal of silicon from silicon wafers to thin the wafers to a desired thickness without inducing residual mechanical stresses into the wafer such as is a result of mechanical thinning operations. The
15 methods of this invention can also be used to provide additional chemical removal of semiconductor surfaces, such as silicon surfaces which have been stressed due to partial mechanical removal of silicon. Additionally the processes can be used in a number of other applications which similarly involve chemical treatment of semiconductor pieces which evolve significant amount of gaseous products.

20 The novel processes include positioning a treated surface of a semiconductor piece, for example wafer 9, into face-to-face relationship with a liquid distributor located within a processing chamber. This is exemplified by liquid distributor 49 and processing chamber 8. A clearance gap 77 exists between the treated surface and the emission ports at the distal ends of the liquid emitters 46. Thereafter the clearance gap is adjusted as desired by
25 controlling and adjusting the position of the liquid distributor operator 51 and attached liquid distributor.

Once the relative position of the semiconductor wafer and liquid distributor have been achieved, it is then possible to begin treatment by emitting the desired processing liquids in non-jetting emission streams. The streams are preferably spill streams which
30 emerge from the emission ports 61 and slowly spill or overflow the spout rims 63 to drain upon the upper partition 43 and down to the drain fitting 44. The spouts are preferably configured to provide level rims which face upwardly. Examples of suitable processing liquids include an aqueous mixture of HF and HNO₃. The emitting step is done by pumping a controlled amount of liquid to supply fitting 67 using a conventional pump (not shown). The
35 flowing liquid causes charging of chamber 47 and flowing of the liquid up through the liquid passageways 65 within emitters 46. The liquid emissions or discharges include forming

meniscuses or liquid domes across the emission ports 61 upon the spouts of the emitters. The formation of these meniscuses is coordinated so that the height of the dome is greater than the clearance gap 77 so that the emitted streams cause an emitter washes or washed areas 78 to be applied to the treated surface of the semiconductor piece 9. Thus the processes include washing the treated surface with a plurality of non-jetting, discrete emitter streams or meniscuses to produce discrete emitter washes upon the treated surface.

The emitting and washing steps are preferably accompanied by rotating the wafer 9 using the rotor and associated rotor drive. This is preferably accomplished at relatively slow rotational speeds in the range of 1-60 rpm, more preferably 5-20 rpm.

The evolution of gases from the reactions occurring at the treated surface are handled by passing produced gases from the washed areas through gas escape passages formed between the liquid emitters, such as about the pillar shaped emitters. The produced gases are then handled by exhausting the gases from the escape passages and from the processing chamber, such as through the exhaust ports 23 and exhaust plenum 29.

The novel methods can further comprise arresting the chemical or other treatment of the semiconductor piece by axially removing the treated surface from the emission streams. This can advantageously be done by lowering the liquid distributor so that the meniscuses no longer wash the treated surface. This is advantageous in stopping the chemical reactions with good precision, to thereby enhance processing reproducibility.

The methods can further include spray rinsing the treated surface such as by spraying a rinsing liquid, such as water, from the spray heads 72 to remove residual processing chemicals.

The wafer can also be dried by passing drying gas through the processing chamber and out via the exhaust ports. Drying gas can be supplied through the spray pipes 72 or via other means (not shown).

Industrial Applicability

The inventions are useful in processing semiconductor materials, particularly in chemical processing where gases are evolved at the surface of the semiconductor material.

Claims

1. A semiconductor processor for processing a treated surface of a semiconductor piece, comprising:

a piece support for supporting said semiconductor piece;

5 a liquid distributor having a plurality of spaced liquid emitters forming an emitter array; said liquid emitters having emission ports through which liquid flows in non-jetting emission streams to provide localized emitter washes upon the treated surface which are wetted by the emission streams against the treated surface;

gas escape passages formed between the liquid emitters to facilitate removal of gases
10 produced at the treated surface by the emission streams;

a liquid collector for recovering liquid supplied through the liquid distributor after the liquid has been presented to the treated surface.

2. A semiconductor processor according to claim 1 wherein the piece support
15 and liquid distributor are mounted so as to provide controlled relative axial motion therebetween to vary the spacing between the liquid emitters and the treated surface.

3. A semiconductor processor according to claim 1 wherein the liquid distributor
is mounted so as to provide controlled axial motion to vary the spacing between the liquid
20 emitters and the treated surface.

4. A semiconductor processor according to claim 1 wherein the piece support
and liquid distributor are mounted so as to provide controlled rotary motion therebetween
to provide increased uniformity of action for the emitted streams upon the treated surface.
25

5. A semiconductor processor according to claim 1 wherein the piece support
is mounted so as to provide controlled rotary motion to provide rotation of the
semiconductor piece relative to the liquid emitters and increased uniformity of action for the
emitted streams upon the treated surface.
30

6. A semiconductor processor according to claim 1 wherein:
the piece support and liquid distributor are mounted so as to provide controlled
relative axial motion therebetween to vary the spacing between the liquid emitters and the
treated surface; and

the piece support and liquid distributor are mounted so as to provide controlled rotary motion therebetween to provide increased uniformity of action for the emitted streams upon the treated surface.

5 7. A semiconductor processor according to claim 1 wherein;
the liquid distributor is mounted so as to provide controlled axial motion to vary the spacing between the liquid emitters and the treated surface;

the piece support is mounted so as to provide controlled rotary motion to provide rotation of the semiconductor piece relative to the liquid emitters and increased uniformity
10 of action for the emitted streams upon the treated surface.

8. A semiconductor processor according to claim 1 and further comprising a gas exhaust port for removing gases which are communicated through the gas escape passages.

15 9. A semiconductor processor according to claim 1 and further comprising spray rinse nozzles for jetting a rinsing spray upon the treated surface.

10 10. A semiconductor processor according to claim 1 and further comprising a plurality of spray rinse nozzles positioned laterally relative to the treated surface for jetting
20 a rinsing spray upon the treated surface.

11. A semiconductor processor according to claim 1 wherein the liquid emitters are upstanding members.

25 12. A semiconductor processor according to claim 1 wherein the liquid emitters are pillars with liquid supply passageways formed therein.

13. A semiconductor processor according to claim 1 wherein the liquid emitters are upstanding spaced pillars with liquid supply passageways formed therein; spacing between
30 said pillars serving to form said gas escape passages.

14. A semiconductor processor according to claim 1 wherein the liquid emitters are pillars with liquid supply passageways formed therein; said liquid supply passageways having cup-shaped passage ends adjacent said emission ports.

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15. A semiconductor processor according to claim 1 wherein the liquid emitters have liquid supply passageways formed therein; said liquid supply passageways having cup-shaped passage ends adjacent said emission ports.

5 16. A semiconductor processor according to claim 1 wherein the liquid emitters have flared spouts at distal ends thereof.

17. A semiconductor processor according to claim 1 wherein the liquid emitters are upstanding members with flared spouts at distal ends thereof.

10

18. A semiconductor processor according to claim 1 wherein the liquid emitters are spaced to provide discontinuous coverage between adjacent emitter washes upon the treated surface which are wetted by the emission streams.

15 19. A semiconductor processor according to claim 1 wherein the piece support is arranged to orient the treated surface of the semiconductor piece downwardly in juxtaposition to an upwardly facing emitter array.

20 20. A semiconductor processor according to claim 1 and further comprising a movable processing head upon which the piece support is mounted.

21. A semiconductor processor according to claim 1 and further comprising a movable processing head upon which the piece support is mounted, and a processing bowl in which the liquid distributor is positioned.

25

22. A semiconductor processor according to claim 1 and further comprising:
a movable processing head;
a rotor mounted for rotation upon the processing head, said piece support being mounted upon the rotor for rotation therewith.

30

23. A semiconductor processor according to claim 1 and further comprising a processing bowl having movable bowl portions upon which are mounted the liquid distributor for axial movement relative to the piece support.

35 24. A semiconductor processor for processing a treated surface of a semiconductor piece, comprising:

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- a frame;
a processing bowl mounted upon the frame;
a processing head mounted upon the frame for controlled movement to and from
said processing bowl;
5 a rotor mounted for controlled rotational movement relative to said frame;
a piece support connected to the rotor for supporting said semiconductor piece
thereon;
a liquid distributor having a plurality of spaced liquid emitters forming an emitter
array; said liquid emitters having emission ports through which liquid flows in non-jetting
10 emission streams to provide localized emitter washes upon the treated surface which are
wetted by the emission streams against the treated surface;
gas escape passages formed between the liquid emitters to facilitate removal of
produced gases from the treated surface by the emission streams;
a liquid collector for recovering liquid supplied through the liquid distributor after
15 the liquid has been presented to the treated surface.

25. A semiconductor processor according to claim 24 wherein the piece support
and liquid distributor are mounted so as to provide controlled relative axial motion
therebetween to vary the spacing between the liquid emitters and the treated surface.
20

26. A semiconductor processor according to claim 24 wherein the liquid
distributor is mounted so as to provide controlled axial motion to vary the spacing between
the liquid emitters and the treated surface.

27. A semiconductor processor according to claim 24 and further comprising a
gas exhaust port for removing gases which are communicated through the gas escape
passages.
25

28. A semiconductor processor according to claim 24 and further comprising spray
30 rinse nozzles for jetting a rinsing spray upon the treated surface.

29. A semiconductor processor according to claim 24 and further comprising a
plurality of spray rinse nozzles positioned laterally relative to the treated surface for jetting
a rinsing spray upon the treated surface.
35

30. A semiconductor processor according to claim 24 wherein the liquid emitters are upstanding members.

31. A semiconductor processor according to claim 24 wherein the liquid emitters
5 are pillars with liquid supply passageways formed therein.

32. A semiconductor processor according to claim 24 wherein the liquid emitters are upstanding spaced pillars with liquid supply passageways formed therein; spacing between said pillars serving to form said gas escape passages.

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33. A semiconductor processor according to claim 24 wherein the liquid emitters are pillars with liquid supply passageways formed therein; said liquid supply passageways having cup-shaped passage ends adjacent said emission ports.

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34. A semiconductor processor according to claim 24 wherein the liquid emitters have liquid supply passageways formed therein; said liquid supply passageways having cup-shaped passage ends adjacent said emission ports.

35. A semiconductor processor according to claim 24 wherein the liquid emitters
20 have flared spouts at distal ends thereof.

36. A semiconductor processor according to claim 24 wherein the liquid emitters are upstanding members with flared spouts at distal ends thereof.

37. A semiconductor processor according to claim 24 wherein the liquid emitters are spaced to provide discontinuous coverage between adjacent emitter washes upon the treated surface which are wetted by the emission streams.

38. A semiconductor processor according to claim 24 wherein the piece support
30 is arranged to orient the treated surface of the semiconductor piece downwardly in juxtaposition to an upwardly facing emitter array.

39. A semiconductor processor for processing a treated surface of a semiconductor piece, comprising:

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a frame;

a processing bowl mounted upon the frame in an upwardly facing orientation;

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a processing head mounted upon the frame for controlled movement to and from said processing bowl; said processing head being constructed to provide mating relationship with the processing bowl to form a substantially enclosed processing chamber;

a rotor mounted for controlled rotational movement relative to said processing head;

5 a piece support connected to the rotor for supporting said semiconductor piece thereon;

a liquid distributor mounted within the processing bowl in subjacent relationship to said piece support when the processing head is in mating relationship with the processing bowl; said liquid distributor having a plurality of spaced upstanding liquid emitters forming
10 an emitter array; said liquid emitters having emission ports through which liquid flows in non-jetting emission streams which spill from upwardly facing spouts of the liquid emitters to provide localized emitter washes upon the treated surface which are wetted by the emissions streams against the treated surface;

gas escape passages formed between the liquid emitters to facilitate removal of
15 produced gases from the treated surface by the emission streams;

a gas exhaust port for removing gases which are communicated through the gas escape passages;

a liquid collector for recovering liquid supplied through the liquid distributor after the liquid has been presented to the treated surface.

20

40. A semiconductor processor according to claim 39 wherein the piece support and liquid distributor are mounted so as to provide controlled relative axial motion therebetween to vary the spacing between the liquid emitters and the treated surface.

25

41. A semiconductor processor according to claim 39 wherein the liquid distributor is mounted so as to provide controlled axial motion to vary the spacing between the liquid emitters and the treated surface.

30

42. A semiconductor processor according to claim 39 and further comprising spray rinse nozzles for jetting a rinsing spray upon the treated surface.

43. A semiconductor processor according to claim 39 and further comprising a plurality of spray rinse nozzles positioned laterally relative to the treated surface for jetting a rinsing spray upon the treated surface.

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44. A semiconductor processor according to claim 39 wherein the liquid emitters are pillars with liquid supply passageways formed therein.

45. A semiconductor processor according to claim 39 wherein the liquid emitters
5 are upstanding spaced pillars with liquid supply passageways formed therein; spacing between said pillars serving to form said gas escape passages.

46. A semiconductor processor according to claim 39 wherein the liquid emitters are pillars with liquid supply passageways formed therein; said liquid supply passageways
10 having cup-shaped passage ends adjacent said emission ports.

47. A semiconductor processor according to claim 39 wherein the liquid emitters have liquid supply passageways formed therein; said liquid supply passageways having cup-shaped passage ends adjacent said emission ports.

15

48. A semiconductor processor according to claim 39 wherein the liquid emitters have flared spouts at distal ends thereof.

49. A semiconductor processor according to claim 39 wherein the liquid emitters
20 are spaced to provide discontinuous coverage between adjacent emitter washes upon the treated surface which are wetted by the emission streams.

50. A semiconductor processor according to claim 39 wherein the piece support is arranged to orient the treated surface of the semiconductor piece downwardly in
25 juxtaposition to an upwardly facing emitter array.

51. A method for processing a treated surface of a semiconductor piece using a liquid, comprising:

positioning said treated surface of the semiconductor piece into face-to-face
30 relationship with a liquid distributor located within a processing chamber; said liquid distributor having an emitter array formed by a plurality of spaced liquid emitters; said positioning defining a clearance gap between spouts of the liquid emitters and the treated surface of the semiconductor piece;

emitting a plurality of non-jetting emission streams of the liquid from the liquid
35 emitters;

said clearance gap being sufficiently close to allow washing localized wash areas of the treated surface with the non-jetting emission streams as the emission streams flow from the spouts of the liquid emitters.

5 **52.** A method according to claim 51 and further comprising passing produced gas from the wash areas through gas escape passages formed between the liquid emitters.

53. A method according to claim 51 and further comprising exhausting gas from the processing chamber.

10

54. A method according to claim 51 and further comprising:
 passing produced gas from the wash areas through gas escape passages formed between the liquid emitters;
 exhausting gas from the gas escape passages and processing chamber.

15

55. A method according to claim 51 wherein said emitting step is accomplished from liquid emitters which are upstanding with spouts at upper ends thereof.

56. A method according to claim 51 wherein said emitting step is accomplished
20 from liquid emitters which have spouts at distal ends thereof; said spouts having rims which are approximately level.

57. A method according to claim 51 wherein said emitting step is accomplished from liquid emitters which are upstanding with spouts at upper ends thereof; said spouts
25 having rims which are approximately level.

58. A method according to claim 51 wherein said emitting step is accomplished by overflowing liquid from upstanding liquid emitters in spill streams.

30 **59.** A method according to claim 51 and further comprising draining liquid emitted by the liquid emitters from the processing chamber.

60. A method according to claim 51 and further comprising adjusting the clearance gap by changing the relative positioning of the piece holder relative to the emitter
35 array.

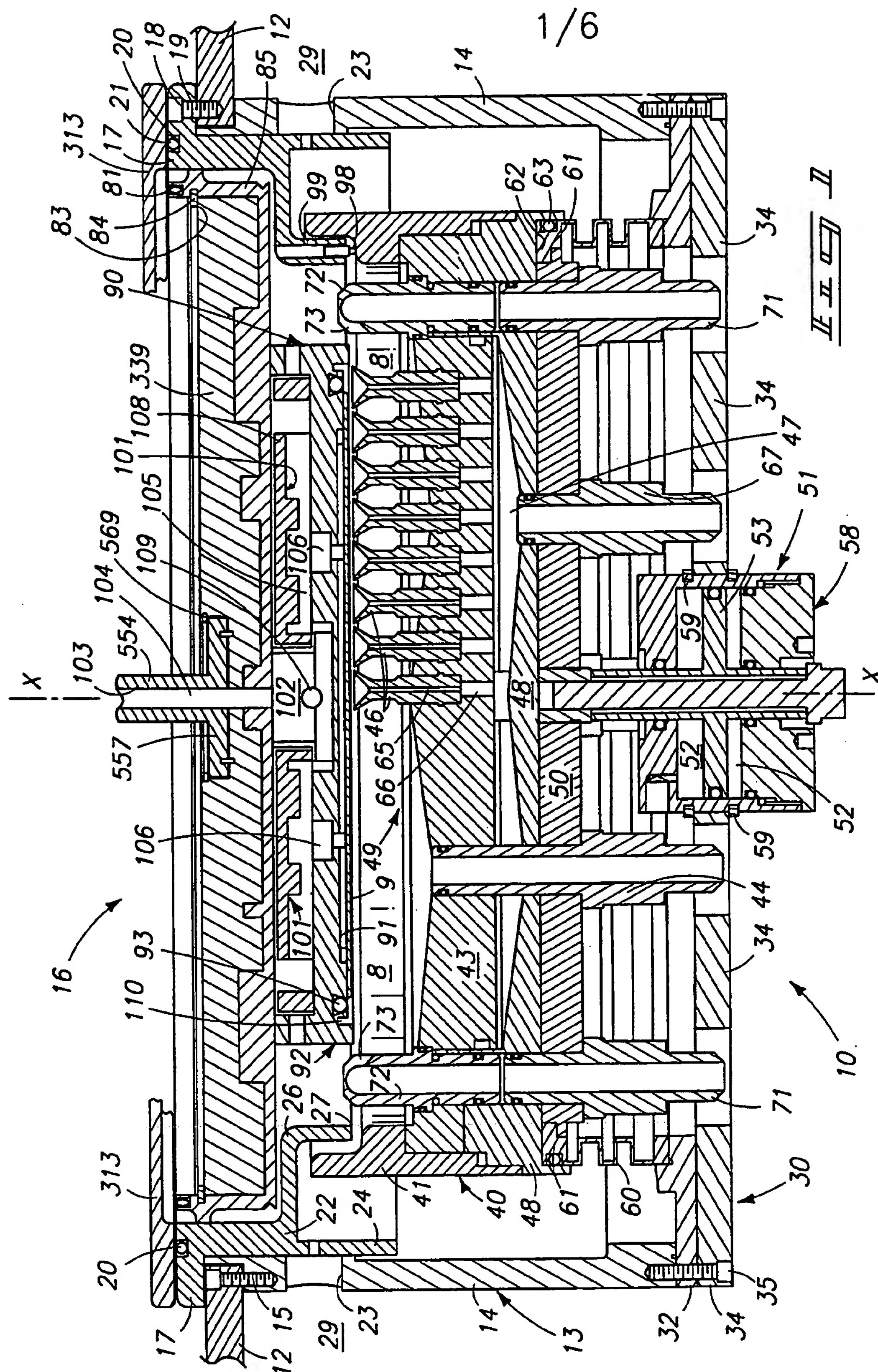
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61. A method according to claim 51 and further comprising adjusting the clearance gap by changing the position of the emitter array.

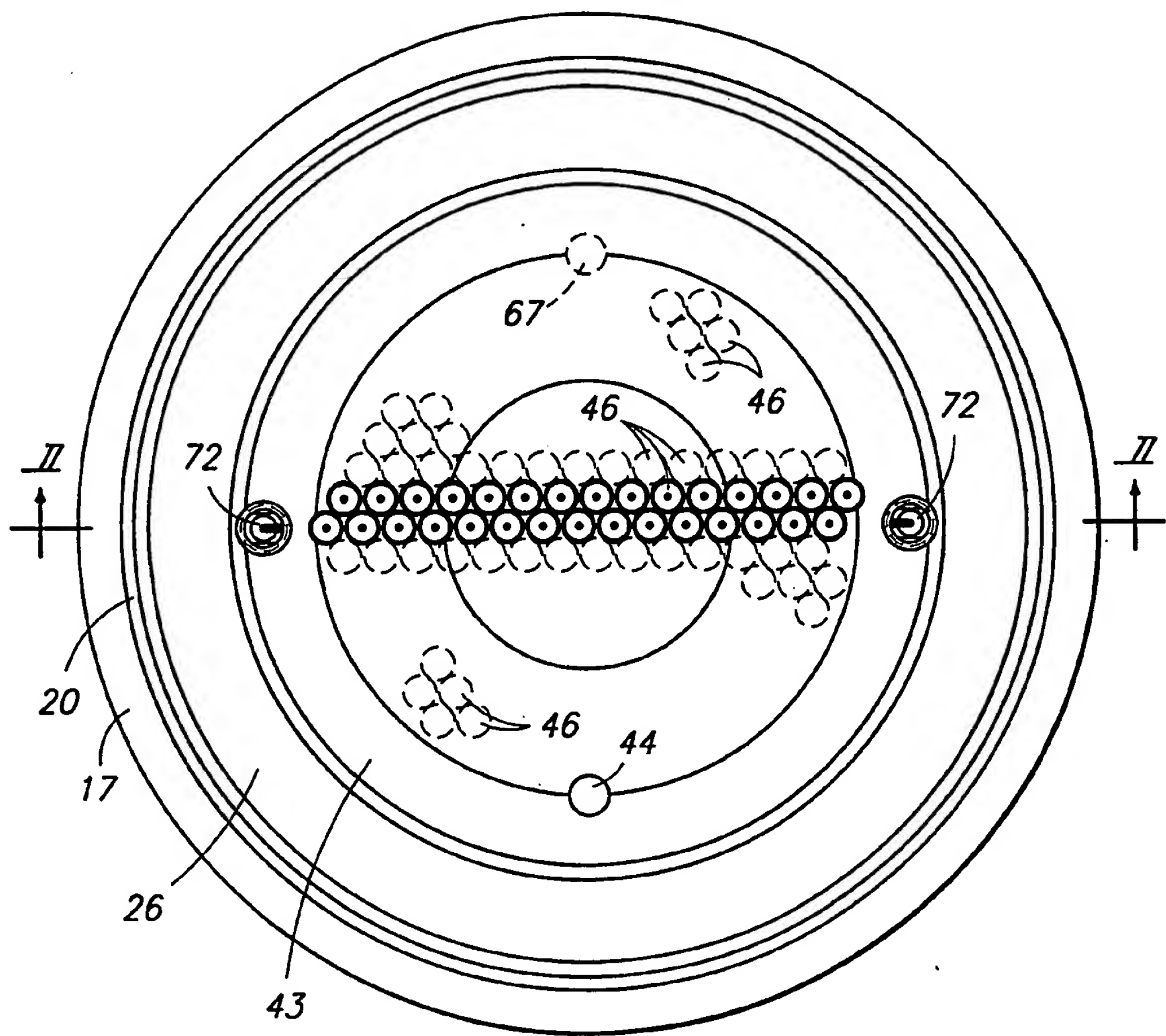
62. A method according to claim 51 and further comprising adjusting the
5 clearance gap by changing the position of the emitter array using a pneumatic operator.

63. A method according to claim 51 and further comprising spraying the treated surface with a rinse liquid.

10 64. A method according to claim 51 and further comprising spraying the treated surface with a rinse liquid from laterally positioned spray heads.



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II II

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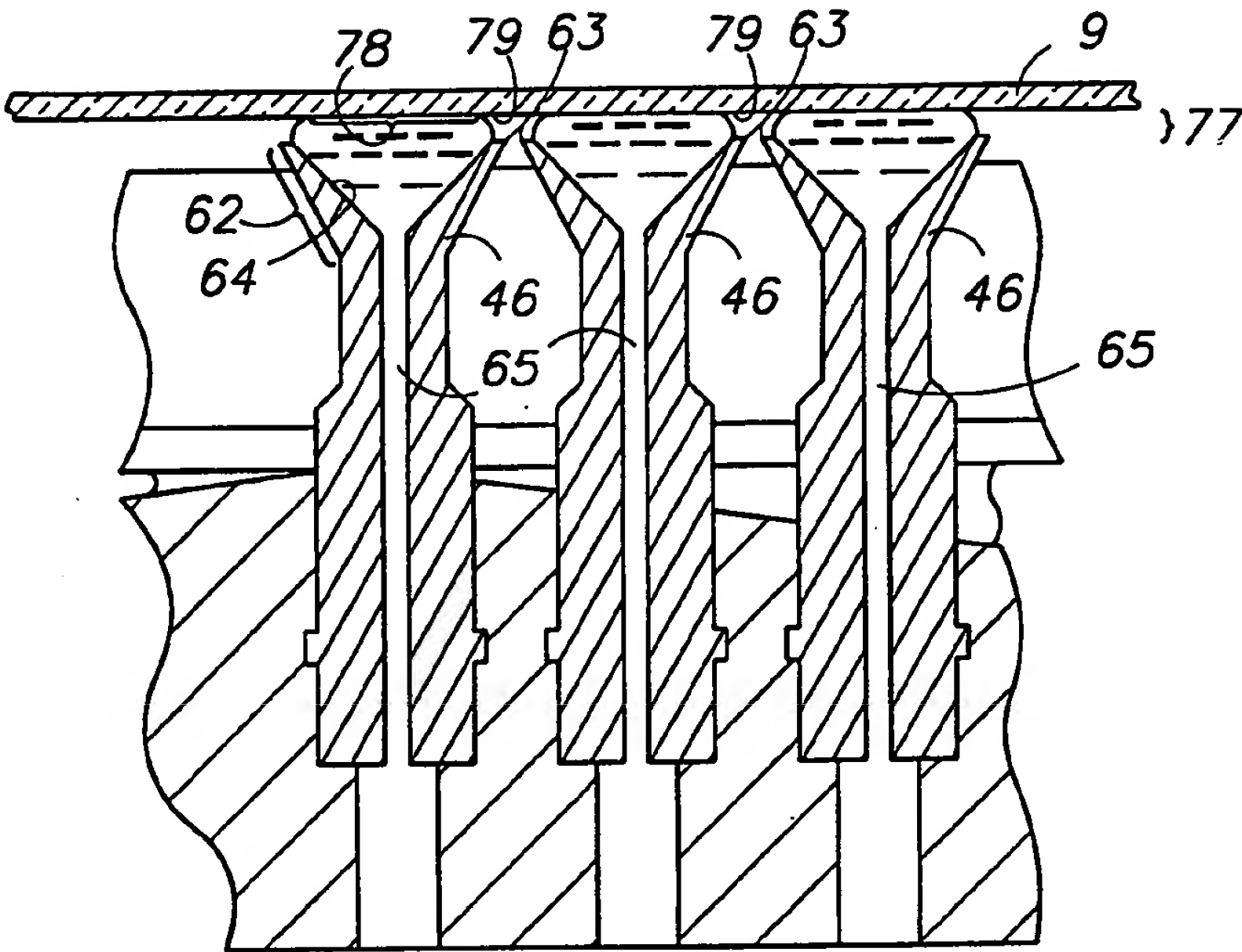
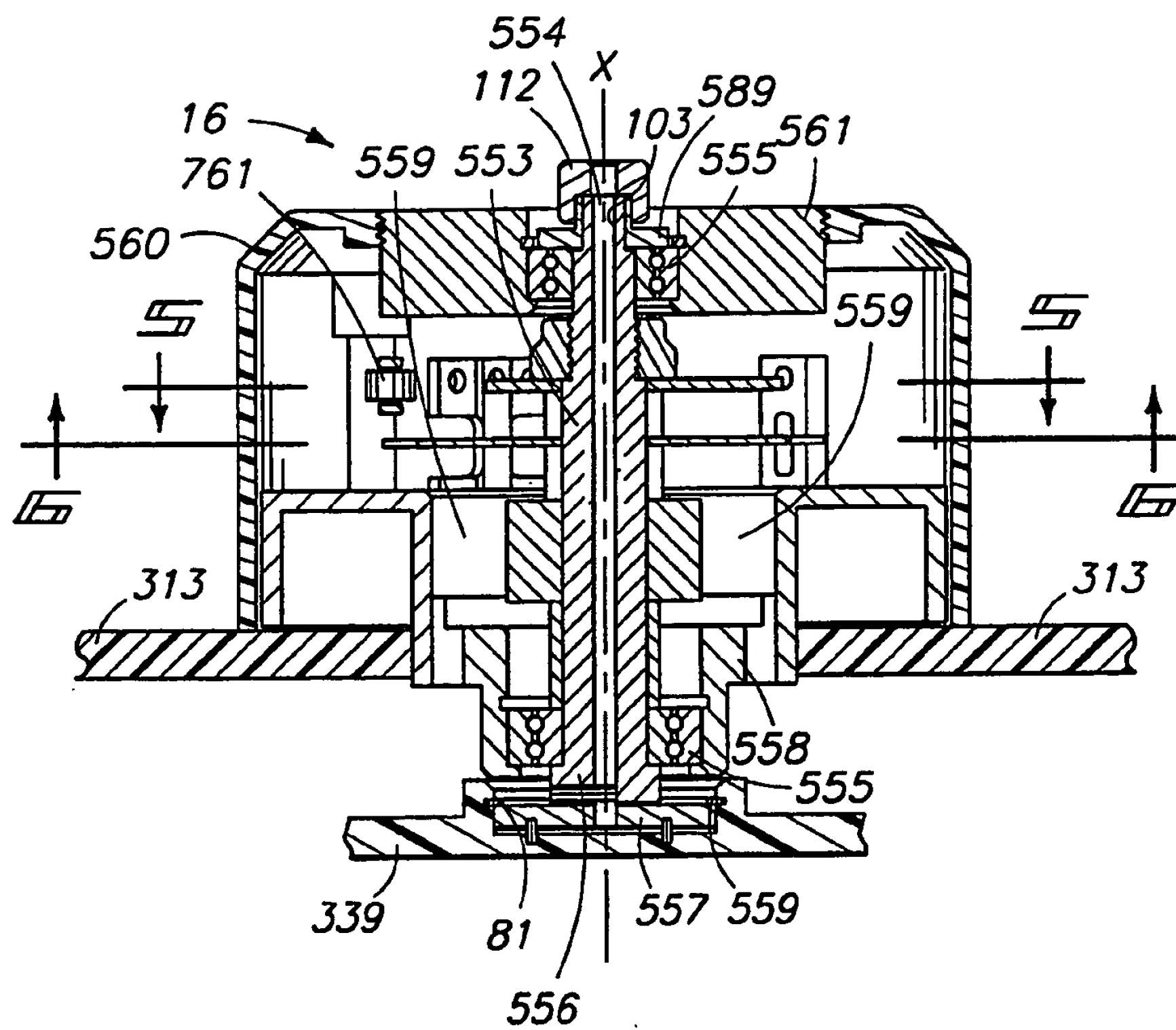


FIG. 3

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Итого 4

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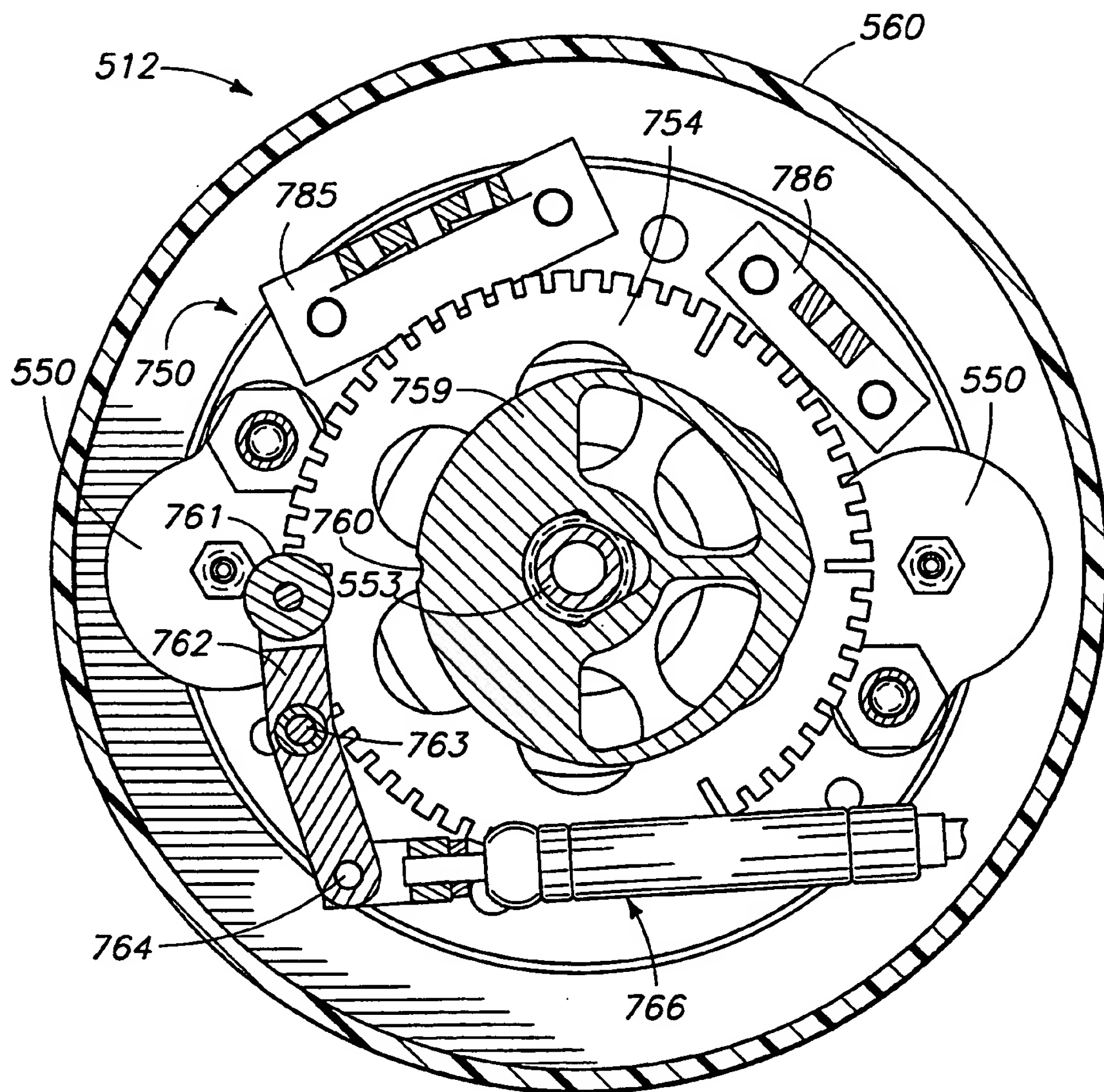


Fig 5

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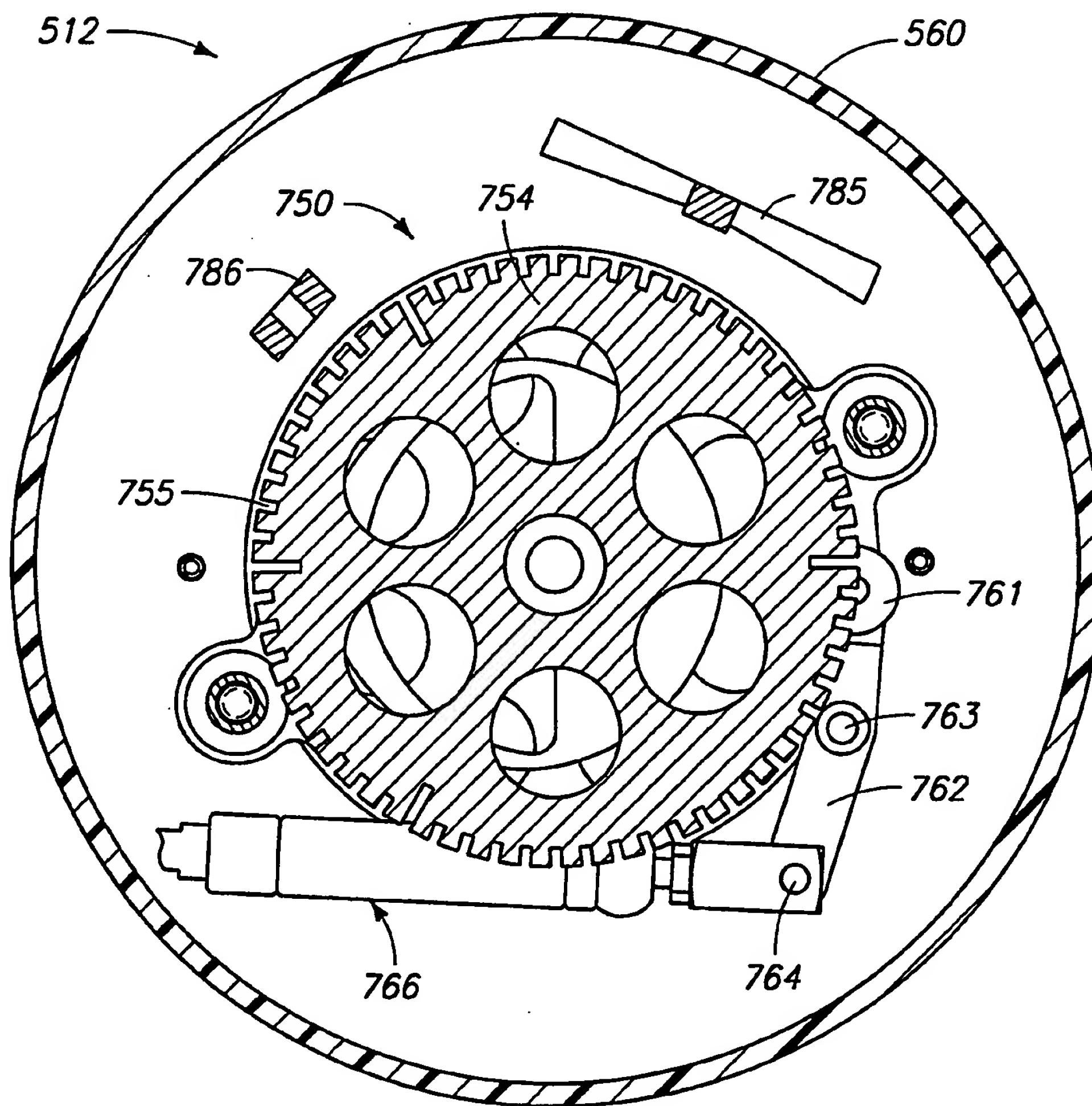


FIG. 6

INTERNATIONAL SEARCH REPORT

Inter. .nal Application No

PCT/US 94/08097

A. CLASSIFICATION OF SUBJECT MATTER
IPC 6 H01L21/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 6 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X A	EP,A,0 434 434 (THORN EMI) 26 June 1991 see column 3, line 27 - column 3, line 47; figure 1	51,58,59 1,11,12, 24,30,31 ,39,44
X A	EP,A,0 481 506 (KABUSHIKI KAISHA TOSHIBA) 22 April 1992 see column 8, line 52 - column 9, line 32; figure 8	51,55 1,11,12, 14,15,24 ,30,31, 33,34,39 ,44,46, 47,58,59
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☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

31 October 1994

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	PATENT ABSTRACTS OF JAPAN vol. 16 no. 170 (E-1194) ,23 April 1992 & JP,A,04 017334 (FUJITSU LTD) 22 January 1992,	51,58,59
A	see abstract ---	1,24,39
A	US,A,5 168 886 (THOMPSON) 8 December 1992 see the whole document -----	1-64

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 94/08097

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EP-A-0481506	22-04-92	JP-A- 4154122	27-05-92
US-A-5168886	08-12-92	US-A- 5235995	17-08-93
		US-A- 5230743	27-07-93
		US-A- 5224504	06-07-93